**HDMI electrical Characteristics**

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| --- | --- |
| **DC Characteristics** | |
| Item | value |
| Input Differential Voltage Level. | 150 |
| Input Common Mode Voltage, | If Sink supports only <=165M:  If sink supports >165Mhz |
|  |  |
| **When source Disabled or disconnected** | |
| Differential Voltage level |  |
| **AC input characteristics** | |
| Minimum differential sensitivity (peak-to-peak) | 150 mV |
| Maximum differential input (peak-to-peak) | 1560mV |
| Max Allowable Intra-Pair Skew at Sink Connector | For TMDS Clock rates 222.75MHz and below:  0.4 Tbit  For TMDS Clock rates above 222.75MHz:  0.15 Tbit + 112psecs |
| Max Allowable Inter-Pair Skew at Sink Connector | 0.2 Tcharacter + 1.78nsecs |
| TMDS Clock Jitter | 0.30 Tbit |
| **Impedance Characteristics** | |
| TDR Rise Time at TP2 (10%-90%) | ≤200 psec |
| Through connection impedance | 100 ohms ±15% |
| At Termination impedance  (when Vicm is within Vicm1 range) | 100 ohms ±10% |
| At Termination impedance  (when Vicm is within Vicm2 range) | 100 ohms ±10% |